

**AMENDMENTS TO THE CLAIMS:**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (currently amended): A method of controlling the power delivered by a heterojunction bipolar transistor power amplifier receiving an input power and delivering an amplified output power in a zero intermediate frequency architecture, said method including a step of detecting said output power and varying a control voltage of said power amplifier by means of a control loop to vary the gain of the amplifier, and a step of varying said input power level of said power amplifier,

wherein, if as long as said output power is greater than a first predetermined limit value, said input power is kept constant and the control voltage is varied to obtain a non-linear said gain, and

wherein, if only when said output power is less than said first predetermined limit value, said input power is reduced reduced, for the same output power, to a value causing said control voltage to be increased to a second predetermined value where the power amplifier has only a linear said gain.

2. (canceled).

3. (currently amended): A circuit for controlling the power emitted by a heterojunction bipolar transistor power amplifier receiving an input power and delivering an amplified output power in a zero intermediate frequency architecture, said circuit including first means, for

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detecting said output power and varying a control voltage, and thus the gain, of said power amplifier, and second means for varying said input power of said power amplifier,

wherein, if as long as said output power is greater than a first predetermined limit value, said second means maintains constant said input power, and said first means varies said control voltage to obtain a non-linear said gain, and

wherein, if only when said output power is less than said first predetermined limit value, said second means reduces said input ~~power-power, for the same output power,~~ to a value causing said control voltage to be increased to a second predetermined limit value where said power amplifier has only a linear said gain.

4. (canceled).
5. (original): The circuit claimed in claim 3 including a variable gain pre-amplifier.
6. (previously presented): The circuit claimed in claim 3 wherein said second means for varying said input power of said power amplifier includes a variable attenuator.
7. (previously presented): A radiocommunication terminal including a power control circuit as claimed in claim 3.